## **ACKNOWLEDGEMENT RECEIPT**

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention

WIRING STRUCTURE FOR INTEGRATED CIRCUIT WITH REDUCED INTRALEVEL CAPACITANCE

Submision Type: Information Disclosure

Statement

**Application Number:** 

10/709204

EFS ID: 106175

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First Named Applicant: Richard Wise

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date
			Produced
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us-ids	fis920030028us1IDS-usidst.xml	1530	2006-04-10
us-ids	us-ids.dtd	7763	2006-04-10
us-ids	us-ids.xsl	12026	2006-04-10
package-data	fis920030028us1IDS-pkda.xml	1755	2006-04-10
package-data	package-data.dtd	27025	2006-04-10
package-data	us-package-data.xsl	19263	2006-04-10
Total files size		69362	

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